

Board of Studies Meeting Minutes # 01

Date : 12/11/2022

Time : 11.00 am – 2.00 pm

Venue : Hybrid Mode(VLSI Lab)

The first BoS meeting (fresh Autonomous status) with University nominee for BoS of the department of ECE, SXCCE was conducted on 12/11/2022 at 11.00 am at VLSI Lab. All the faculty members of the department attended the meeting along with an University nominee for BoS, two expert members from outside the college, one representative from Industry and one Postgraduate Meritorius Alumnus. Dr.S.Absa /Asst.Prof/Department of ECE formally welcomed the gathering. Our Principal Dr.J.Maheshwaran gave the introductory speech. Dr.S.Caroline, HoD Department of ECE presented the Curriculum and Syllabi of UG. Dr.Jeraldin Auxillia, PG Co-ordinator/Applied Electronics presented the Curriculum and Syllabi of M.E- Applied Electronics. Dr.T.Latha, PG Co-ordinator/Communication Systems presented the Curriculum and Syllabi of M.E- Communication Systems. Dr.C.Helen Sulochana PG Co-ordinator/Medical Electronics presented the Curriculum and Syllabi of M.E- Medical Electronics.

University nominee for BoS

1. Dr.B.Sathyabama, Professor, Department of Electronics and Communication, Thiagarajar College of Engineering, Madurai-625015.

Expert Members in the Subject from outside the college

1. Dr.E.S.Gopi, Associate Professor, Department of ECE, NIT, Trichy
2. Dr.J.Sheeba Rani, Associate Professor, Department of Avionics, Indian Institute of Space Science and technology (IIST), Trivandrum.

Representative from Industry

1. Dr.R.Solomon Roach, Senior Design Lead Engineer, Tessolve Semiconductor Pvt. Ltd., Chennai

Postgraduate Meritorius Alumnus

1. Ms.A.Sahaya Shiny, Associate Tech Lead, CapeStart Software Pvt Ltd

Agenda for the Meeting

1. Draft curriculum, credits, distribution of courses for UG
2. Draft curriculum, credits, distribution of courses for PG
3. Draft Syllabus

Discussion

1. Draft curriculum, credits, distribution of courses for UG

- Categorize the subjects based on Electronics, Communication and allied..
- CO-PO mapping should be refined.
- Recent text books should be included.
- NPTEL links can also be added in reference.
- Recent trends related elective subject should be included.
- Make the students to do Python programming on UNIX platform.
- System C can be included in the soft skill subject.
- Few concepts of OOPs can be added.

Electrical and Instrumentation Engineering

- Reduce the content of the syllabus. Difficult to learn this in the first semester.
- We can give suggestion to EEE department to change the topics.

Electronic Devices and Circuits

- Very fundamental topics can be clubbed into a single module.
- Reduce the content of the syllabus.
- Reduce lecture hours for fundamental modules and increase the lecture hours for other modules.
- Basic two-port networks can be included in the syllabus.

Electronic Devices and Circuits Lab

- Shift all circuit experiments first then diode based experiments according to the theory syllabus.

Matrix and Calculus

- Transforms should be included. Since we have to apply different transform concepts in core subjects.
- Give a generic title for this subject.

General

- MPMC can be added as a 2 credit subject with practical sessions.
- Industrial training should be arranged through Placement Cell.
- Change the title of the subject Transmission Lines and RF Systems into RF Communication.
- Include antenna based subject in Professional Electives.

2. Draft curriculum, credits, distribution of courses for PG

M.E-Applied Electronics


- Department faculty should give the topics to be included in the mathematics subject based on the need of the mathematical concept in the course.
- Include Power Electronics as a core subject in this curriculum.
- Semiconductor for MOSFETs can also be included.
- Research Tool course can be included along with the research methodology.
- If the students are attending a NPTEL course ,they should get the Elite grade and the credit will be transferred to the academic.

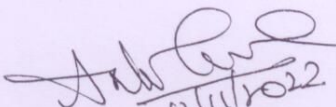
ME Communication System:

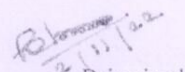
- In Research Methodology subject include research tool also.
- Change the subject Name Technical Seminar to Mini Project.
- Modify Maths syllabus common for all PG programmes.
- Microwave integrated circuit syllabus majority of the topics are from microwave circuit , so change the subject name as microwave circuits.
Prerequest needed(basics of CMOS/Analog VLSI)
- Subjects to be included as Electives:
 - Advance communication network
 - Advance information theory and coding
 - VLSI Signal Processing/Architecture
 - MATHS include optimization as a unit

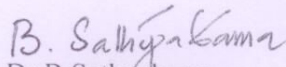
ME Medical Electronics:

- Get the suggestions from the experts in the field of medical.
- Human anatomy subject can be included as core paper.

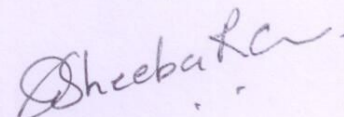

HoD
12/11/22

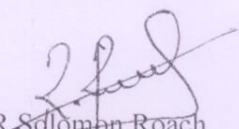

Academic Dean
12/11/22

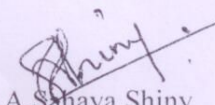

Principal
12/11/22

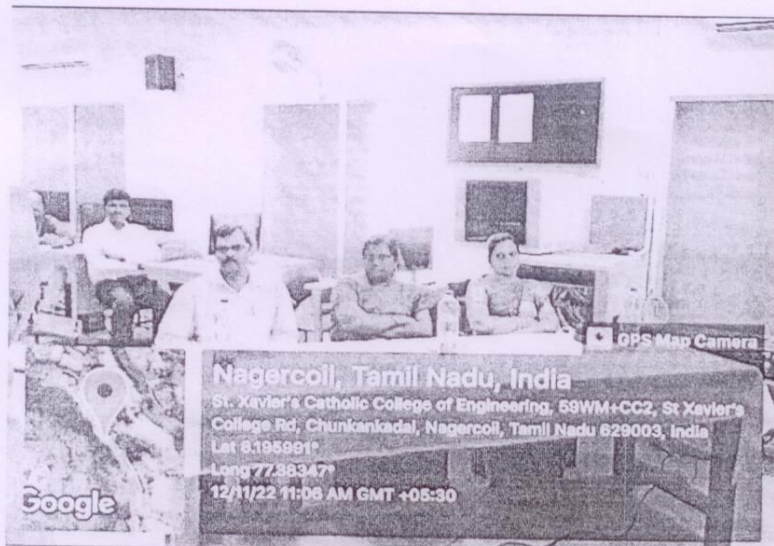
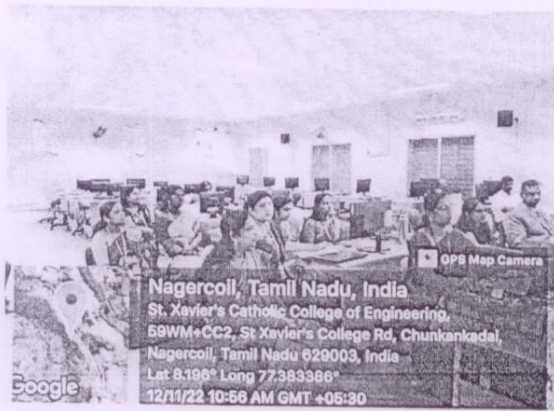

Dr.B.Sathyabama.
(University nominee for BoS)

Dr.E.S.Gopi
(Expert Members)


Dr.J.Sheeba Rani
(Expert Members)


Dr.R.Solomon Roach
(Representative from Industry)


Ms.A.Shaya Shiny
(Meritorius Alumnus)



VLF-Applied Electronics

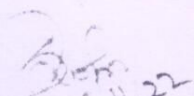
- Department faculty should give the topics to be included in the mathematics subject based on the need of the mathematical concept in the course
- Include Power Electronics as a core subject in this curriculum
- Semiconductor for MOSFETs can also be included.
- Research Tool course can be included along with the Research methodology.
- If the students are attending a NPTEL course they should get the full grade and the credit will be transferred to the academic

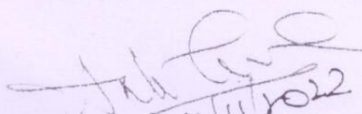
ME Communication System:


- In Research Methodology subject include research tool also.
- Change the subject Name Technical Seminar to Mini Project
- Modify Maths syllabus common for all PG programmes.
- Microwave integrated circuit syllabus majority of the topics are from microwave circuit, so change the subject name as microwave circuits
Prerequisite needed(basics of CMOS Analog VLSI)
- Subjects to be included as Electives:
 - Advance communication network
 - Advance information theory and coding
 - VLSI Signal Processing Architecture
 - MATHS include optimization as a unit

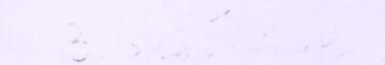
ME Medical Electronics:

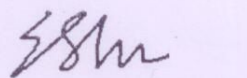
- Get the suggestions from the experts in the field of medical.
- Human anatomy subject can be included as core paper.

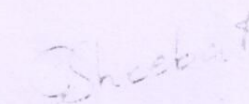

21/11/22

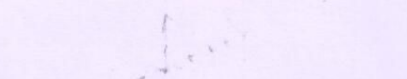

21/11/22
Academic Dean



Principal


Dr. B. Sathyabama
(University nominee for BoS)


Dr. F. S. Gopi
(Expert Members)


Dr. J. Sheeba Rani
(Expert Members)


Dr. R. S. Jeyaraj
(Representative from Industry)


Ms. A. Sahaya Shiny
(Meritorius Alumnus)

St. Xavier's Catholic College of Engineering, Chunkankadai, Nagercoil – 629003


Department of Electronics and Communication Engineering

Board of Studies Meeting

Date : 12/11/2022

Venue : VLSI Lab

MEMBERS PRESENT

Sl. No.	Name & Designation	Signature
1	DR J MANGESWARAN, PRINCIPAL, SXCCG	
2	B. SATHYA BAMA, PROFESSOR, ECE/TCE.	B. Sathya Bama
3	J. Sheeba Rani Associate Prof	Sheeba Rani
4	R. Solomon Roach Sr. Design Lead	R. Solomon Roach
5	SABARA SHINYA Associate Tech Lead	Shinya
6	Dr. S. CAROLINE, Asst. Professor	Dr. S. Caroline
7	Dr. D Jeraldin Auxilia prof	Dr. D Jeraldin
8	Dr. E. Helen Subachona	Dr. E. Helen
9	Dr. T. Latha	Dr. T. Latha
10	Dr. Y.R. Annie Bessant, AP/ECE	Y.R. Annie Bessant
11	Dr. T. Ajitha / AP	T. Ajitha
12	C. RENIT / AP	C. Renit
13	V. Femila Savio / AP	V. Femila Savio
14	Dr. M. Felix Nes Mabel / AP, Maths	M. Felix Nes Mabel

15	Dr. L. Femila, AP/ECE	<i>L. Femila</i>
16	K. Baby Lisa, AP/ECE	<i>K. Baby Lisa</i>
17	Dr. B. C. Preethi, AP/ECE	<i>B. C. Preethi</i>
18	Dr. S. ABBA, AP/ECE	<i>S. ABBA</i>
19	A. Anitha, AP/ECE	<i>A. Anitha</i>
20	Dr. S. Mary Vasanthi, AP/ECE	<i>S. Mary Vasanthi</i>
21	G. ARUN KING	<i>G. ARUN KING</i>
22	Dr. A. MILTON	<i>A. MILTON</i>
23	M. STARWIN	<i>M. STARWIN</i>
24	Dr. D. JUDSON	<i>D. JUDSON</i>
25	T. Mary Little Flower, AP/ECE	<i>T. Mary Little Flower</i>
26	S. Christo Ejin Ray AP/ECE	<i>S. Christo Ejin Ray</i>
27	S. Selvin Roadap Kumar, AP/ECE	<i>S. Selvin Roadap Kumar</i>
28	A. C. Jinsla AP/ECE	<i>A. C. Jinsla</i>
29		
30		